

SUBSYSTEMS: DATA ACQIUSTION

MODEL	#	#	Power Supply Requirements		Power Down Iq uA	Input Voltage Range	Conversion Rate	Accuracy or Linearity		Differential Linearity		Full Scale Error		Zero Error		Bipolar Zero Error		Voltage Reference		Tristate Output Latches	I/O	Model Designator Temperature				# of Pins	Starting Price /100's
			Min	+Icc mA				Lsb's		Lsb's		Lsb's		Lsb's		Lsb's		INT	EXT			0	-25	-40	-55		
			+Vcc					+25C	Tmax	+25C	Tmax	+25C	Tmax	+25C	Tmax	+25C	Tmax					70C	85	85	125		
AD7824	8	4	5	20	na	+5V	2.5	(Total unadjusted error, 1 LSB)										+5V	Yes	P8	K	B		T	24	\$10.45	
AD7824	8	4						(Total unadjusted error, 1/2 LSB)													L	C		U		\$14.45	
AD7828	8	8	+5	20	na	+5V	100	(Total unadjusted error, 1 LSB)										+5V	Yes	P8	K	B		T	28	\$10.95	
AD7828	8	8						(Total unadjusted error, 1/2 LSB)													L	C		U		\$14.95	
AD7825	8	4	+3	2.5	10	0>Vref	500	1	1	1	1	(Total unadjusted error, 1 LSB)				2.5	No	P8			A		24	\$5.50			
AD7829	8	8	+3	2.5	10	0>Vref	500	1	1	1	1	(Total unadjusted error, 1 LSB)				2.5	No	P8			A		28	\$7.25			
AD8401	with 1, 8 bit D/A							Total unadjusted error= ± 3 LSB's																			
AD8401	8	4	+5V	13		+3V	500	1	1	1	2	4	2	4			1.25	No	P8			G		28	\$9.00		
AD7777	10	4	+5	10	100	Vbias-Vswing	400	1	1	1	1	12	12	12	12		+2		P10			A		28	\$9.75		
AD7778	10	8	+5	10	100	Vbias-Vswing	400	1	1	1	1	12	12	12	12		+2		P10			A		44		\$10.75	
AD7579	10	2 diff	+5	10	na	0>Vref	50	1	1	0.9	0.9	5	5	2	2		+2.5V	Yes	P8	J	A		S	24	\$9.90		
AD7579	10	2 diff	+5	10				1/2	1/2					1	1						K	B			\$16.42		
AD7580	10	2 diff	+5	10	na	0>Vref	50	1	1	0.9	0.9	5	5	2	2		+2.5V	Yes	P10	J	A		S	24	\$9.90		
AD7580	10	2 diff	+5	10				1/2	1/2					1	1						K	B			\$16.42		
AD7811	10	4	+2.7	2.5	10	0>Vref	500	1	1	1	1	(Total unadjusted error, 1 LSB)				2.5	No	S				A		16	\$3.60		
AD7812	10	8	+2.7	2.5	10	0>Vref	500	1	1	1	1	(Total unadjusted error, 1 LSB)				2.5	No	S				A		20	\$4.05		
AD7818	10	4	+2.7	1.3	10	0>Vref	100	1	1	1	1	2	2	1	1		+2.5		S			A		8	\$3.60		
AD974	16	4	+5	15	10	ALL	200KSPS	3	3	3	3	333		65			+2.5V		S			A		28			
AD974	16	4	+5	15	10	ALL	200KSPS	2	2	1 3/4	1 3/4	165	28	65	8	65	8	+2.5V				B					
AD7858 with Self calibration)																											
AD7858	12	8	+3V	6	10	Vref/2	200	1	1	1	1	1	1	1	1	1	1	2.5		S			A		24	\$11.35	
AD7858					10	or ±Vref/2		1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2					B			\$14.50		
AD7858L (Low Power version of AD7858)																											
AD7858L	12	8	+3V	1.9	10	Vref/2	100	1	1	1	1	1	1	1	1	1	1	2.5					A			\$8.15	
AD7858L	12	8			10	or ±Vref/2		1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2			S			B			\$10.50	
AD7859, Self & System calibration																											
AD7859	12	8	+5V	6	10	Vref/2	200	1	1	1	1	1	1	1	1	1	1	2.5		Yes	P12			A		24	\$11.35
AD7859					10	or ±Vref/2		1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2						B			\$14.50	
AD7859L (Low Power version of AD7859)																											
AD7859	12	8	+3V	1.9	10	Vref/2	100	1	1	1	1	1	1	1	1	1	1	2.5		Yes	P12			A	S	24	\$8.15
AD7859	12	8				or ±Vref/2		1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2	1/2						B			\$10.50	
AD7888	12	8	+2.7	5.225	6 to 700	0>Vref	200	2	2	1	1	3	3	3	3		+2.5V		S				A		16	\$4.50	
AD7888	12	8						1	1														B			\$6.00	
AD7890-10	12	8	5	10	15	±10V	100	1	1	1	1	2.5	2.5	2	2	4	4	+2.5V		S			A	S	24	\$12.00	
AD7890-10	12	8			15			1/2	1/2														B			\$15.80	
AD7890-4	12	8			15	+4.096																					
AD7890-2	12	8			15	+2.5																					
AD7891-10	12	8	5	10	15	±10V	500	1	1	1	1	2.5	2.5	2	2	4	4	+2.5V		S/P12					44	\$20.00	
AD7891-10	12	8			15		300	1/2	1/2																		
AD7891-2	12	8			15	+2.5																					
AD7856	14	8	+5V	17		0 to Vref	285	2	2	2	2	10	10	10	10	10	10	4.096		S			A	S	24	\$18.00	
AD7856	14	8	1					1	1	1	1											K				\$18.00	